

determining a second orientation for a second plurality of conductive paths based on said analyzed characteristics; and  
designing the structure having the first plurality of conductive paths arranged in the first orientation and the second plurality of conductive paths in the determined second orientation.

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8. (Amended) A method of designing a printed circuit board comprising:  
analyzing at least one characteristic of a first plurality of relatively parallel conductive paths on said printed circuit board, said first plurality of relatively parallel conductive paths being arranged in a pattern in a first area of said printed circuit board;  
rearranging said pattern of conductive paths based on the analyzed at least one characteristic such that a second plurality of relatively parallel conductive paths in a second area of said printed circuit board have a different geometry with respect to one another as compared to a geometry of said first plurality of relatively parallel conductive paths in said first area; and  
designing the printed circuit board having the first plurality of relatively parallel conductive paths and the second plurality of relatively parallel conductive paths.

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16. (Amended) A method comprising:  
analyzing a characteristic of a first plurality of conductive paths arranged in a first pattern;

altering said characteristic by rearranging said pattern to form a second pattern;  
and  
designing a structure to have said first pattern and said second pattern.

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a4 19. (Amended) The method of claim 16, wherein altering said characteristic comprises determining the second pattern for a second plurality of conductive paths based on said analyzed characteristic.

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